

Bhoj Reddy Engineering College for Women
Department of Electronics & Communication Engineering
Time Table for the Academic Year 2015-16
II B.Tech II Semester ECE Section-C

Room No : 404 North Block (NB)

w.e.f : 21-12-2015

Day	930-1030	1030-1130	1130 - 1230	1230 - 1330	1330 - 1430	1430-1530	1530-1630
Monday	<-----ECPC/ ET LAB----->			LUNCH	ECA(G1-404) PDC(G2-403) EMTL(G3-411)	PEE	DDV
Tuesday	ES	ECA	PDC		<----- ET/ ECPC LAB----->		
Wednesday	ECA	EMTL(G1-404) ECA(G2-403) PDC (G3-411)	DDV		PDC	EMTL	ES
Thursday	<-----GS LAB----->		EMTL		ECA	DDV	PEE
Friday	PEE	ES	GS		PDC(G1-404) EMTL (G2-403) ECA(G3-411)	EMTL	PDC
Saturday	DDV	EMTL	PDC		PEE	ES	ECA

S.No	Subject		Faculty	Dept
1	PEE	Principles of Electrical Engineering	G. Poorna	EEE
2	ECA	Electronic Circuit Analysis	K. Anjaneyulu	ECE
3	PDC	Pulse and Digital Circuits	Radhika.R	ECE
4	ES	Environmental Studies	Y. Swetha	BS
5	EMTL	Electromagnetic Theory and Transmission Lines	S.Deepika	ECE
6	DDVHDL	Digital Design using Verilog HDL	S.Archana Subhash	ECE
7	GS	Gender Sensitization	K. Sunitha	ENGLISH
8	ET LAB	Electrical Technology Lab	G. Poorna, GSL. Vani	EEE
9	ECPC LAB	Electronic Circuits and Pulse Circuits lab	Radhika.R, K. Anjaneyulu	ECE
10	GS LAB	Gender Sensitization Lab	K. Sunitha	ENGLISH

Tutorial Groups :

G1: 4C1-4E4

G2: 4E5-4G9

G3: 4H0-L436